

図1

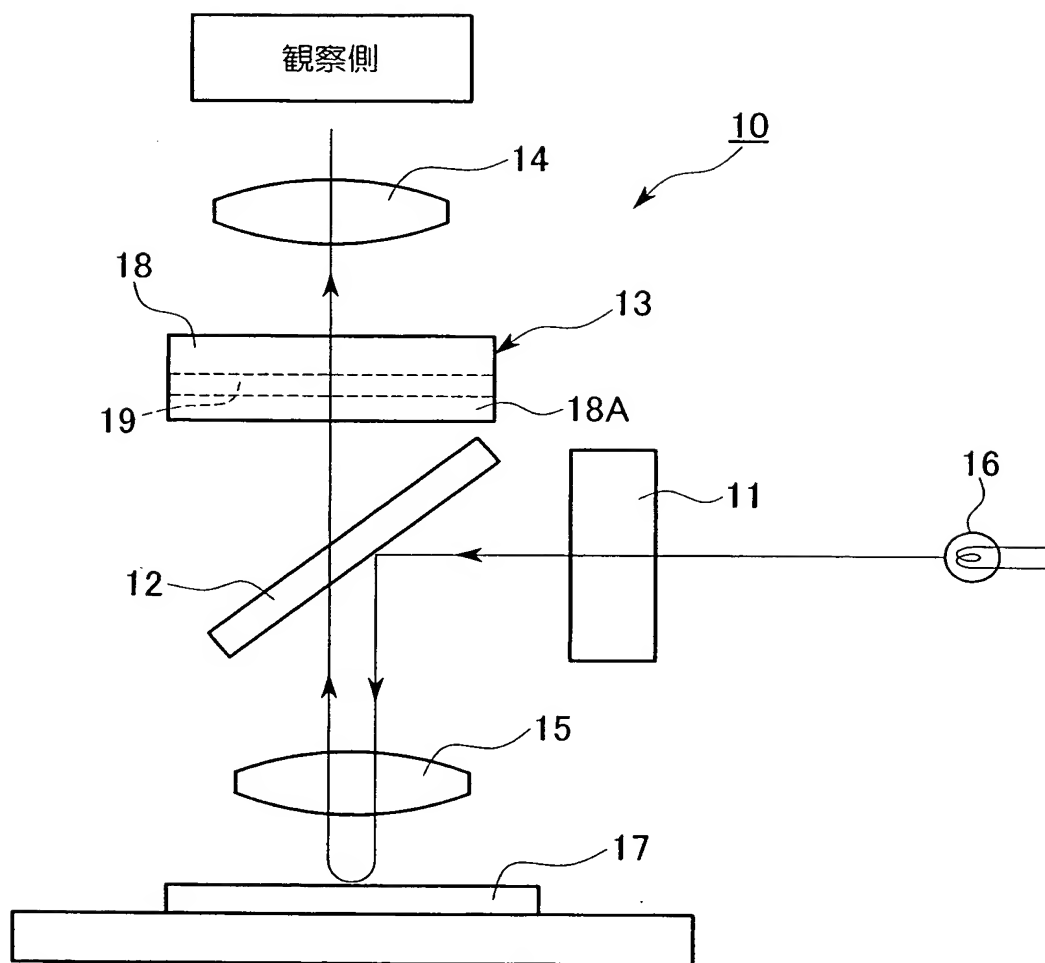


図 2 A

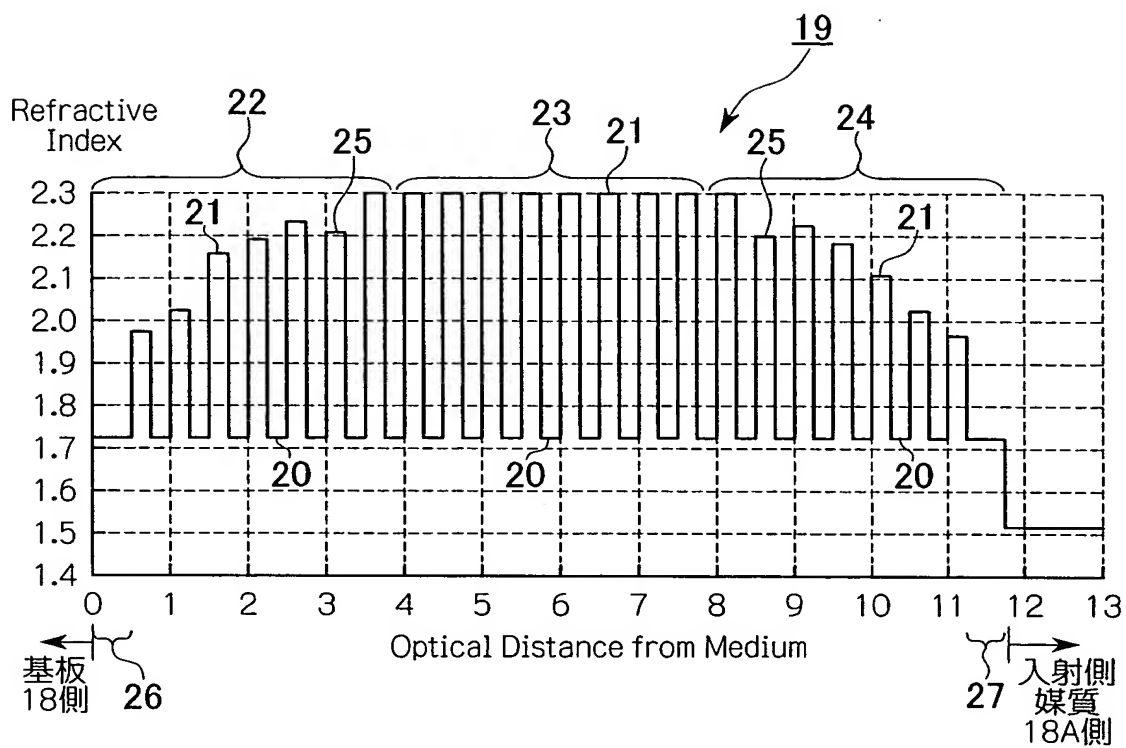


図 2 B

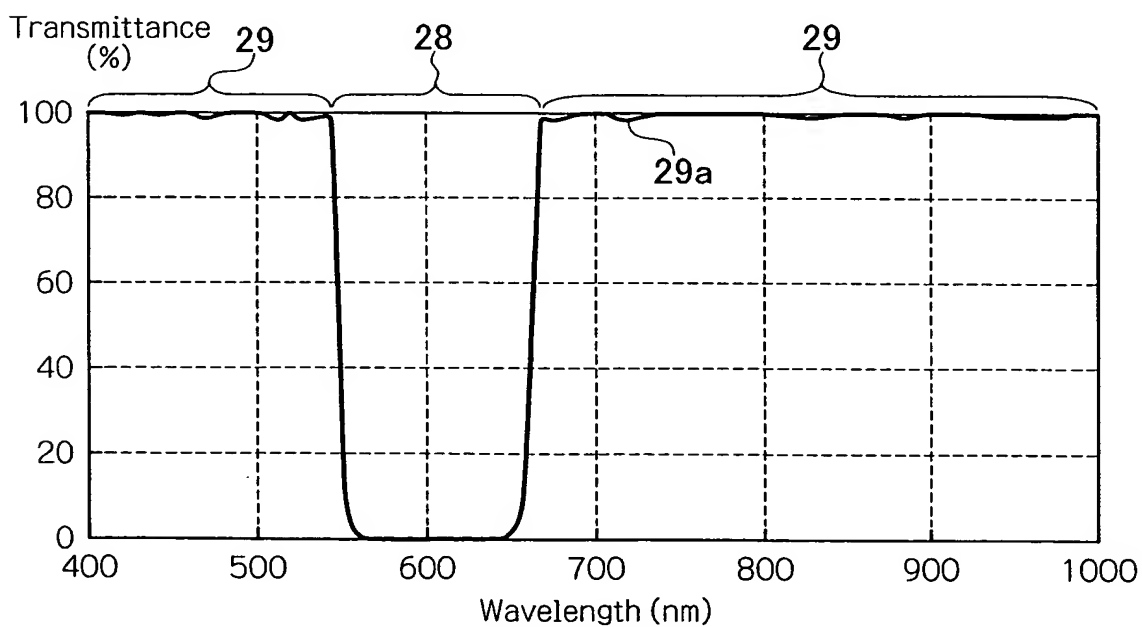


図 3

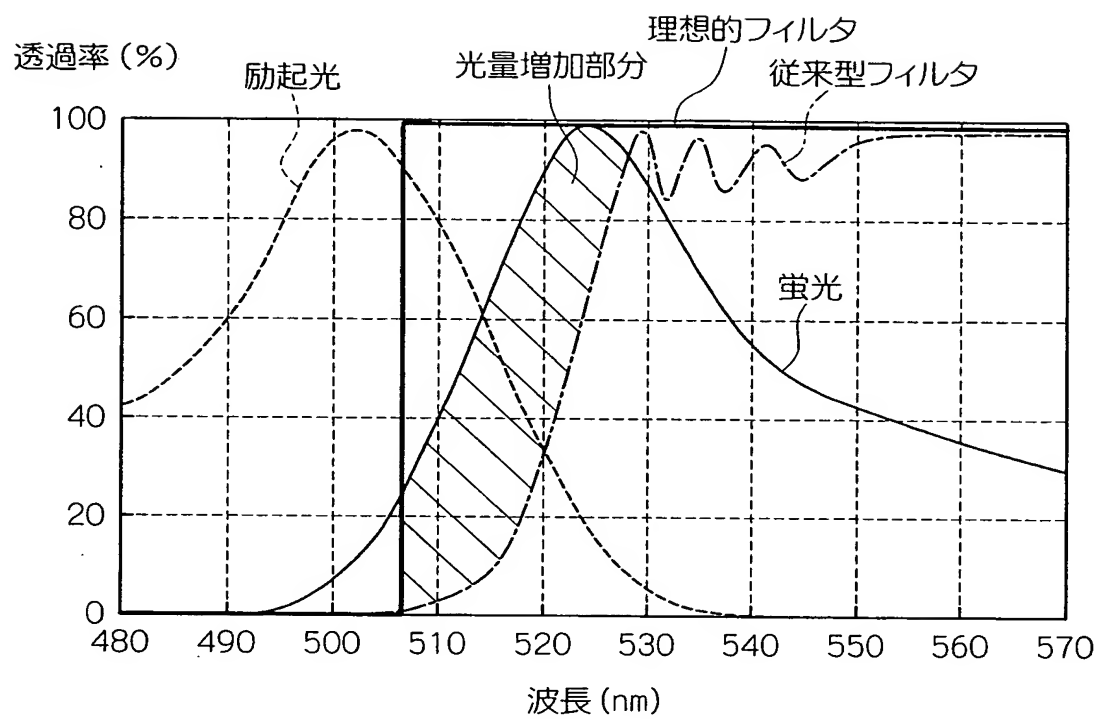


図 4 A

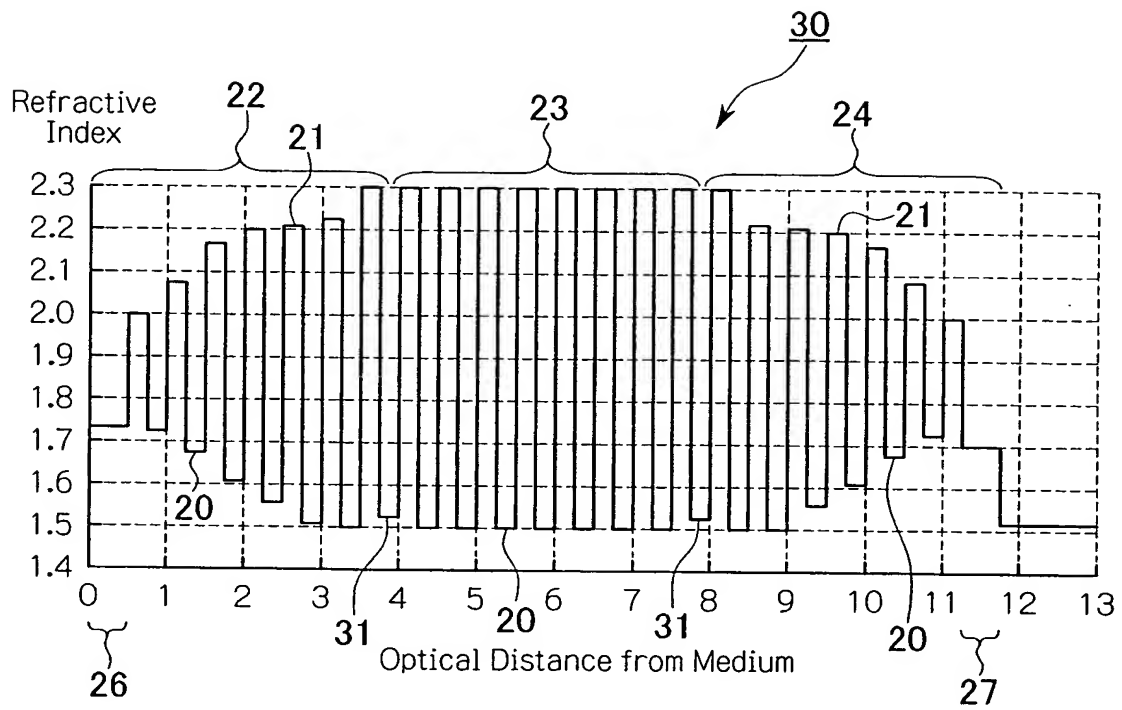


図 4 B

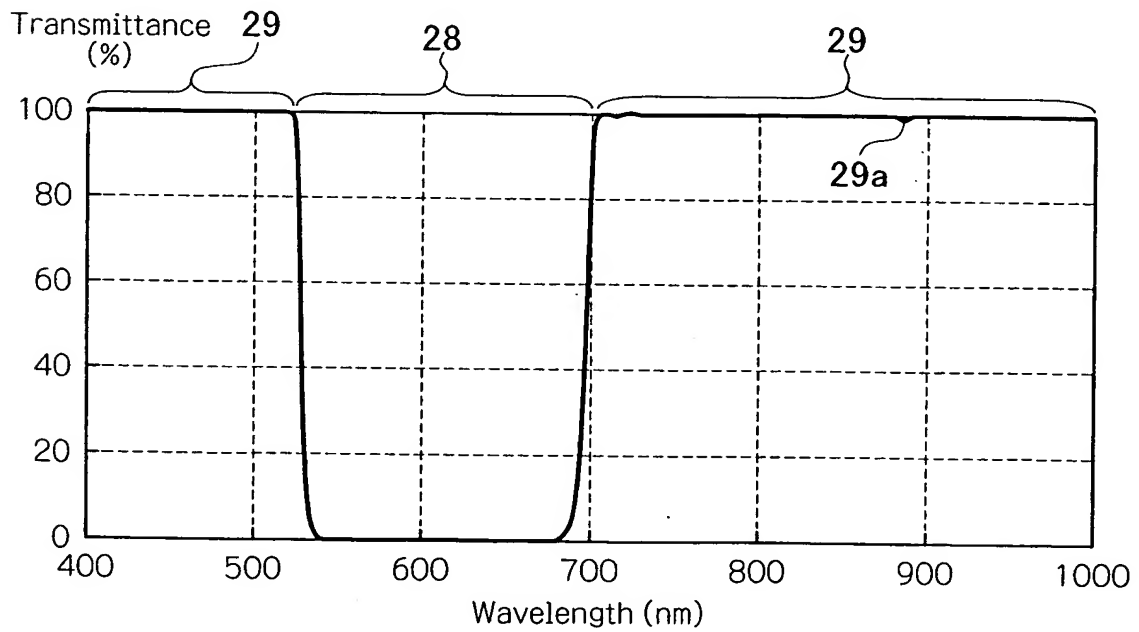


図 5 A

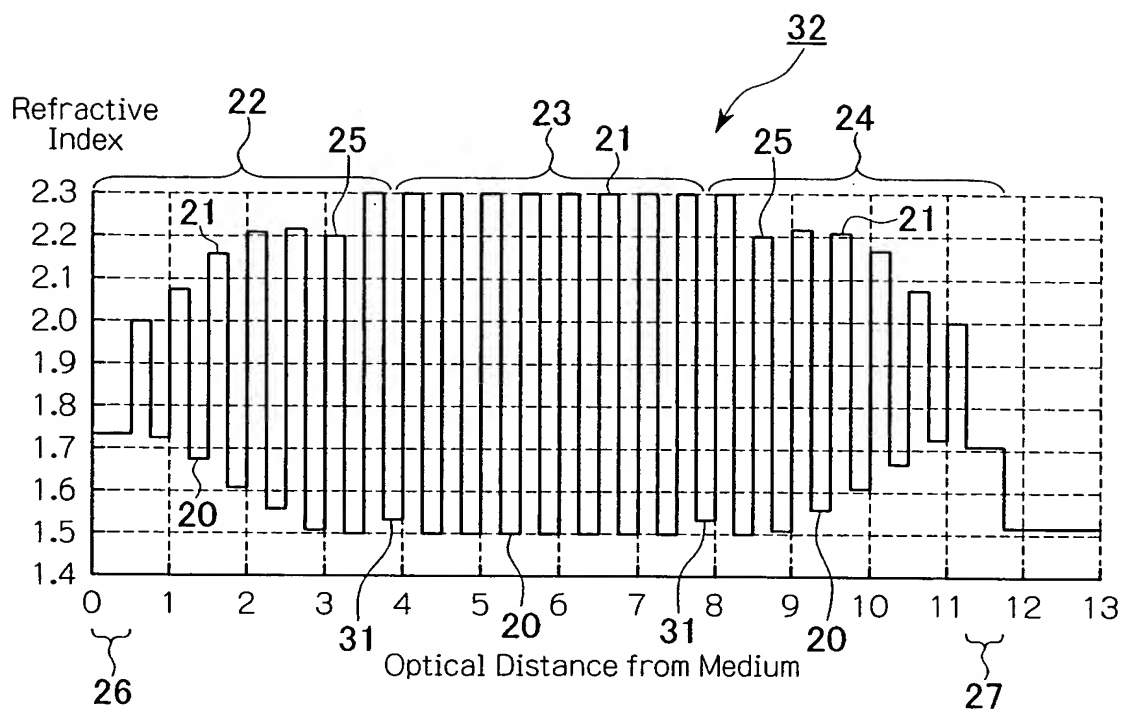


図 5 B

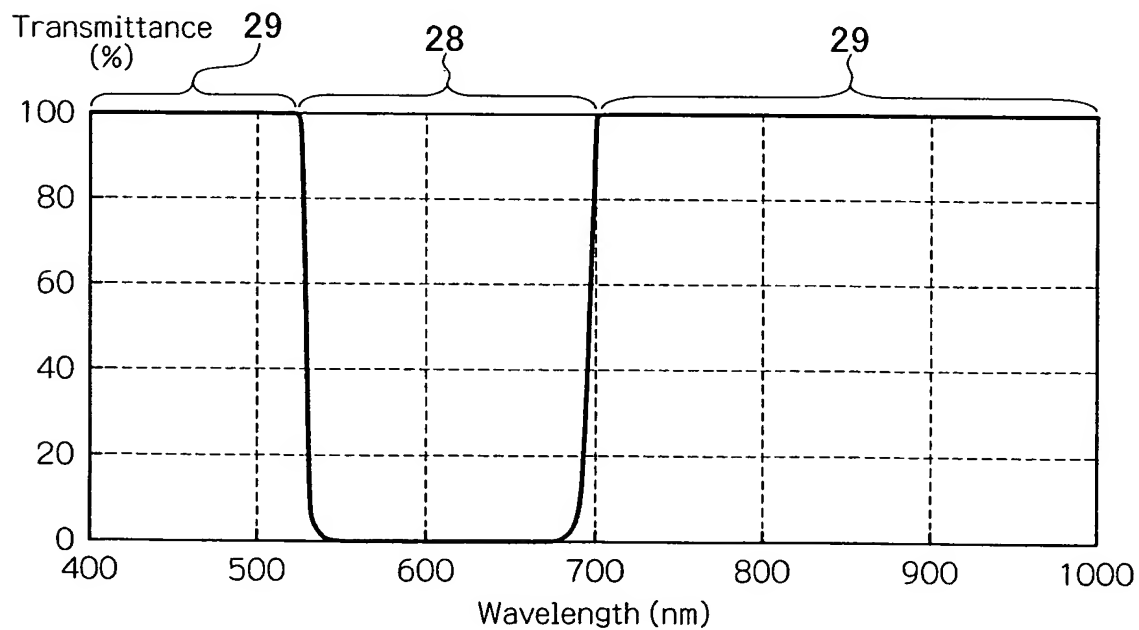


図 6 A

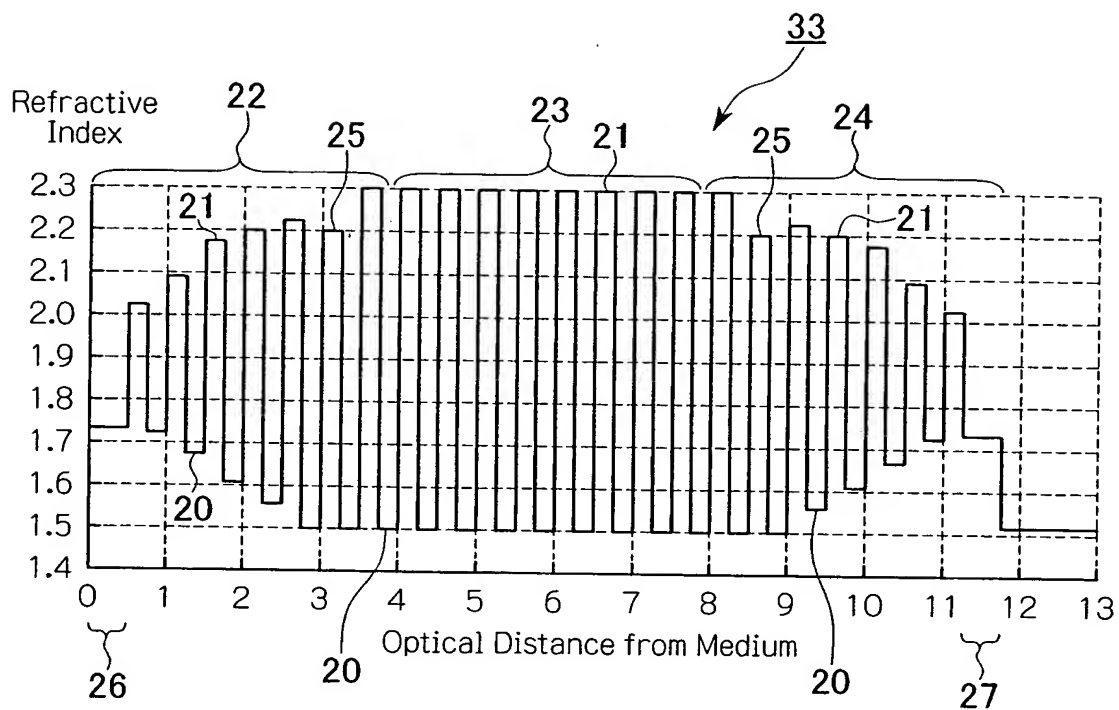


図 6 B

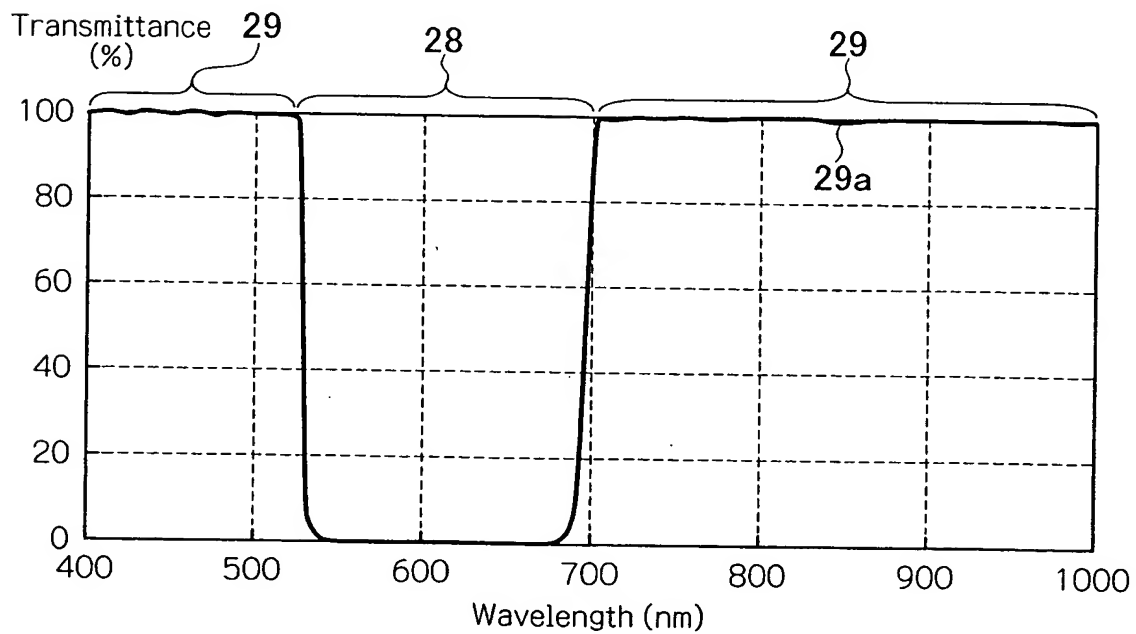


图 7 A

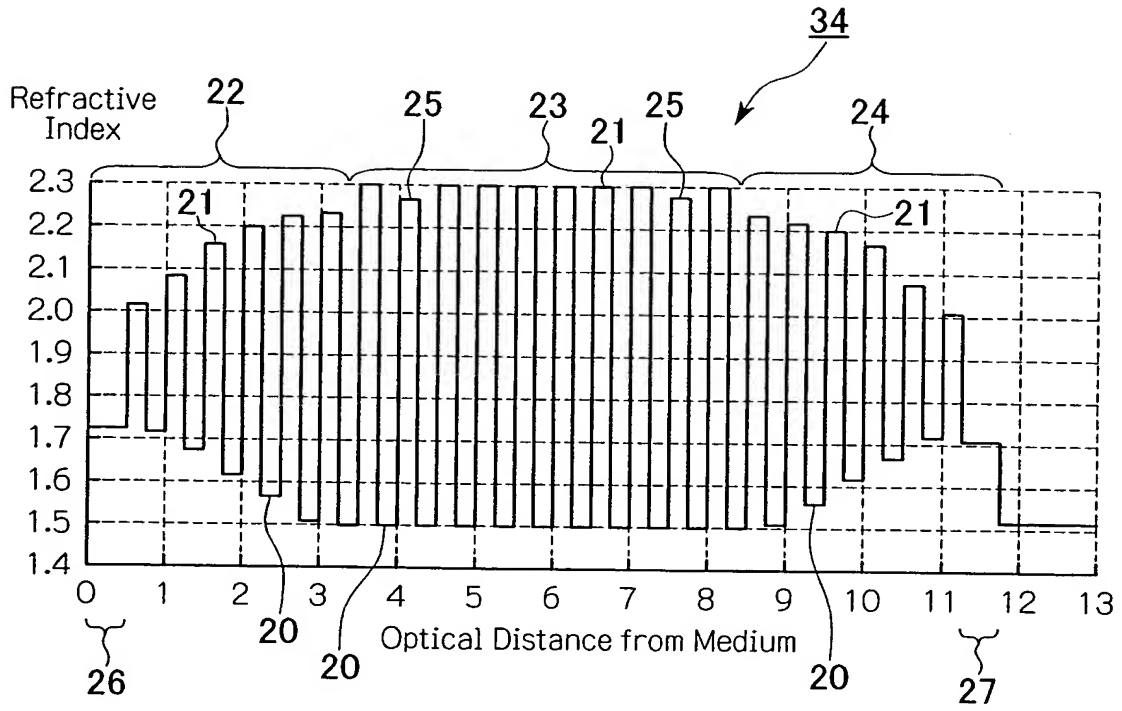


図 7 B

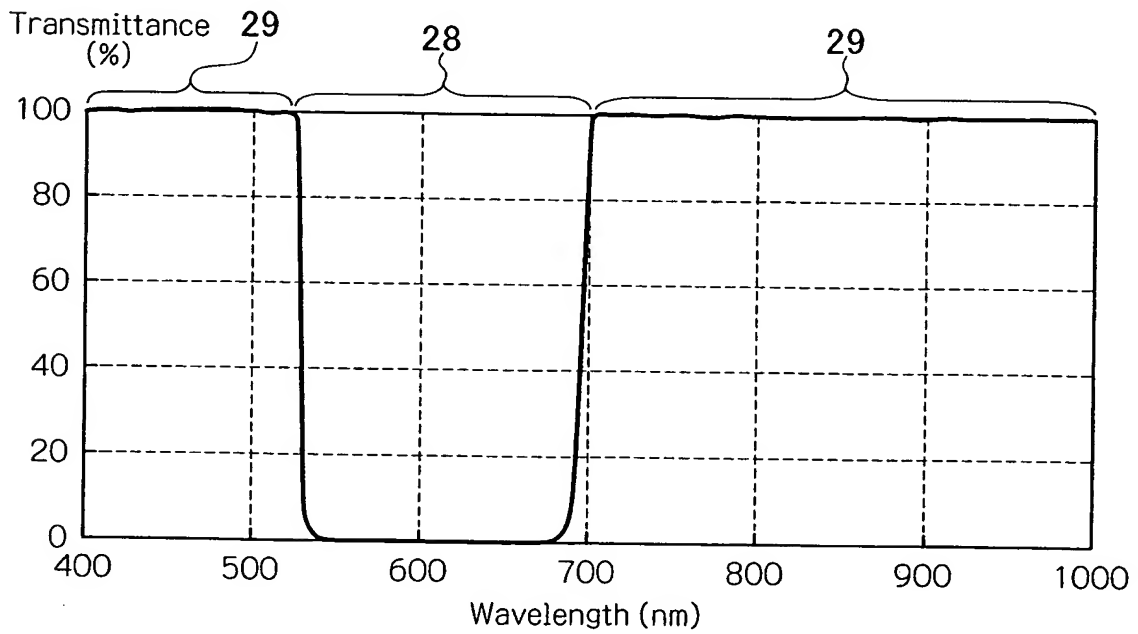


図 8 A

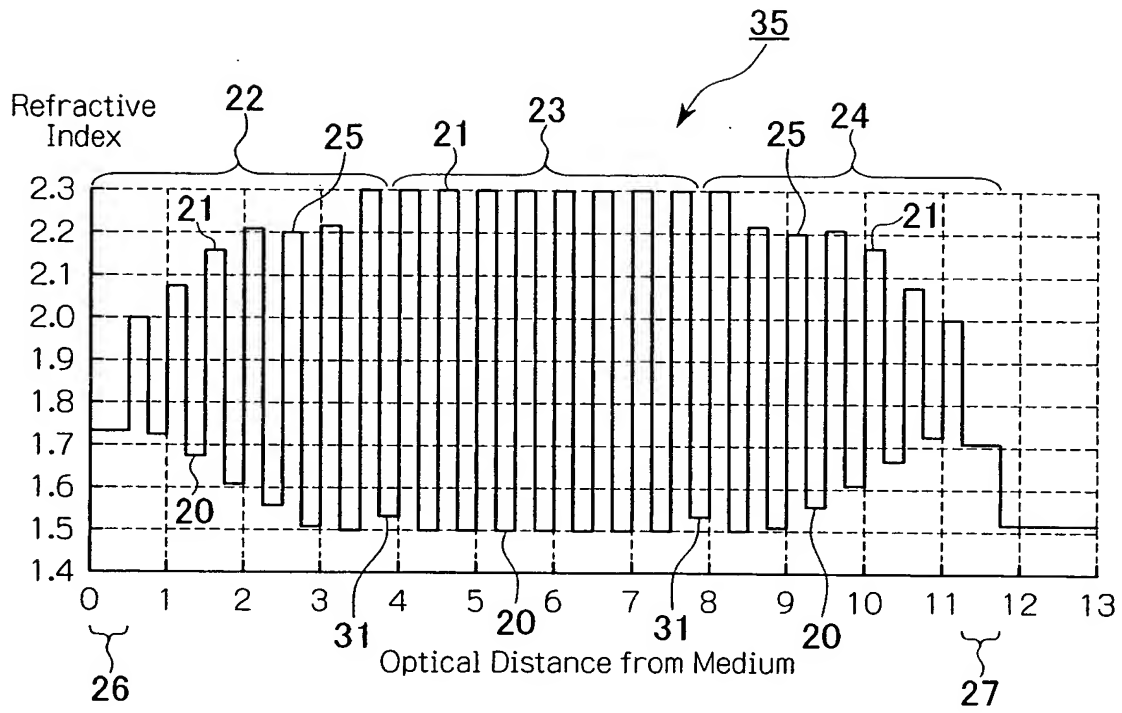


図 8 B

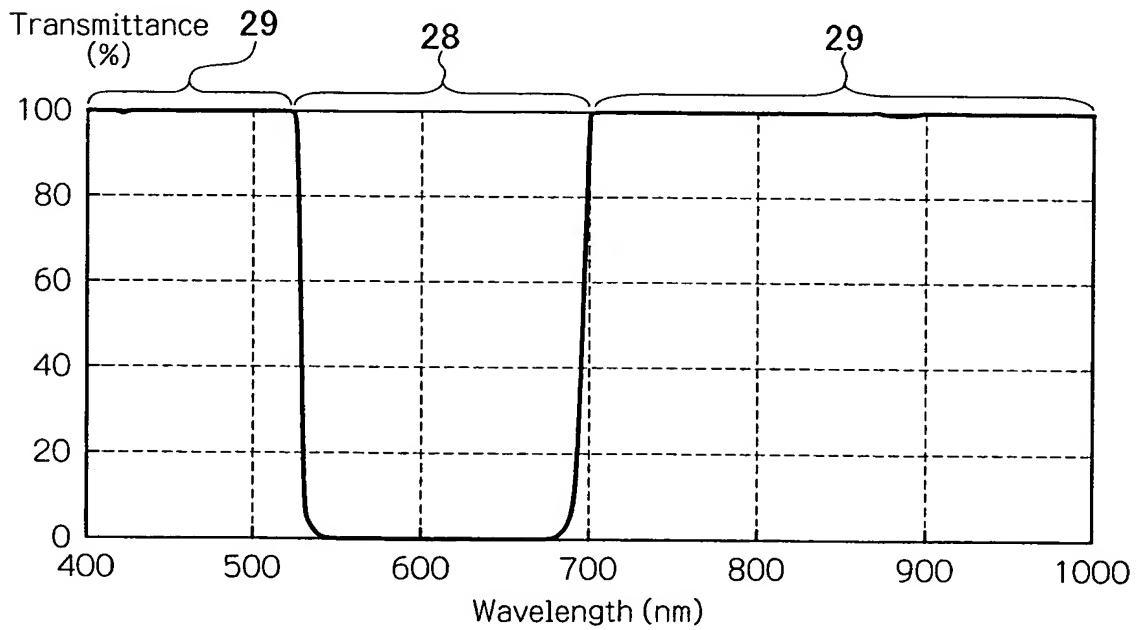




图 9 A

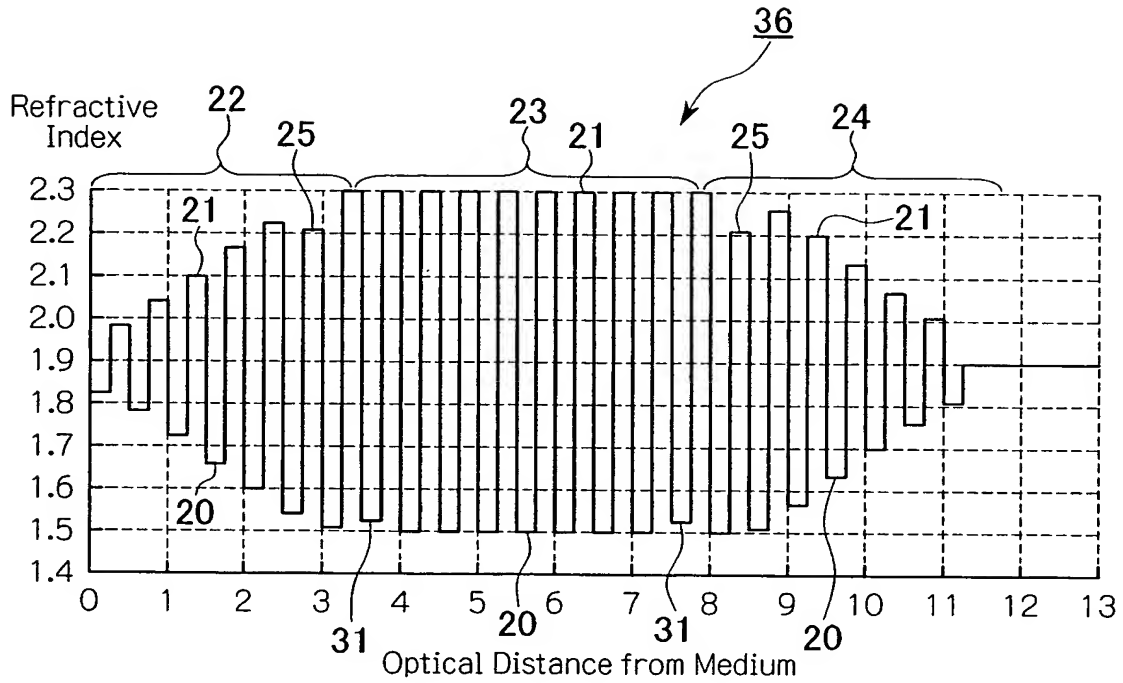


图 9 B

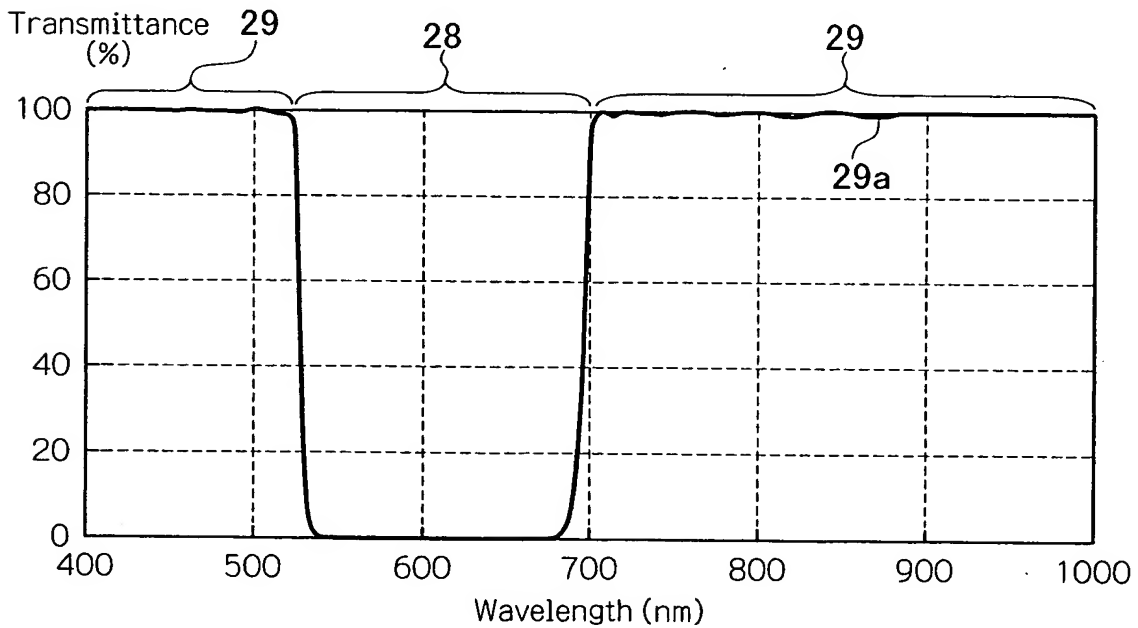


図 1 0 A

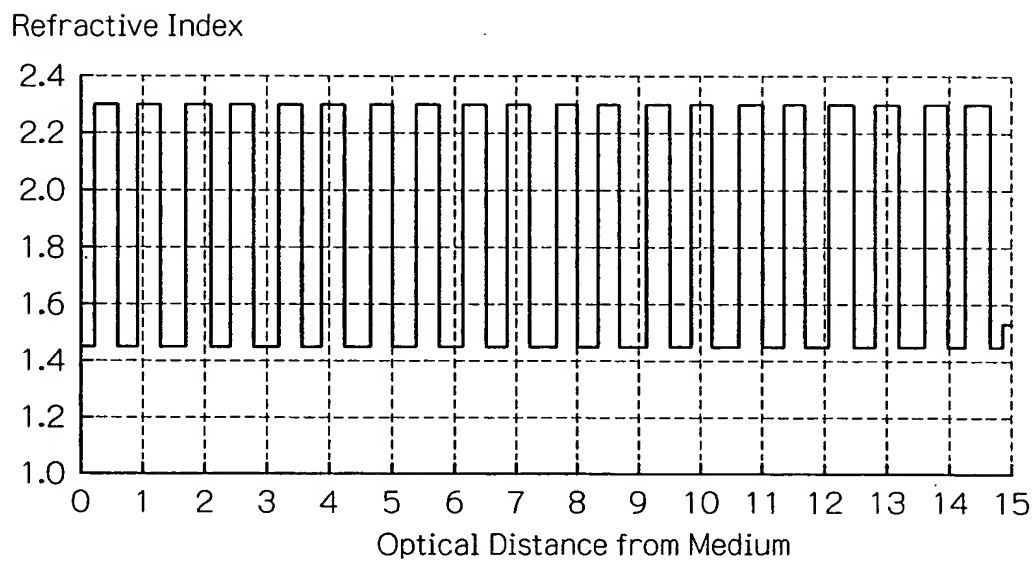


図 1 0 B

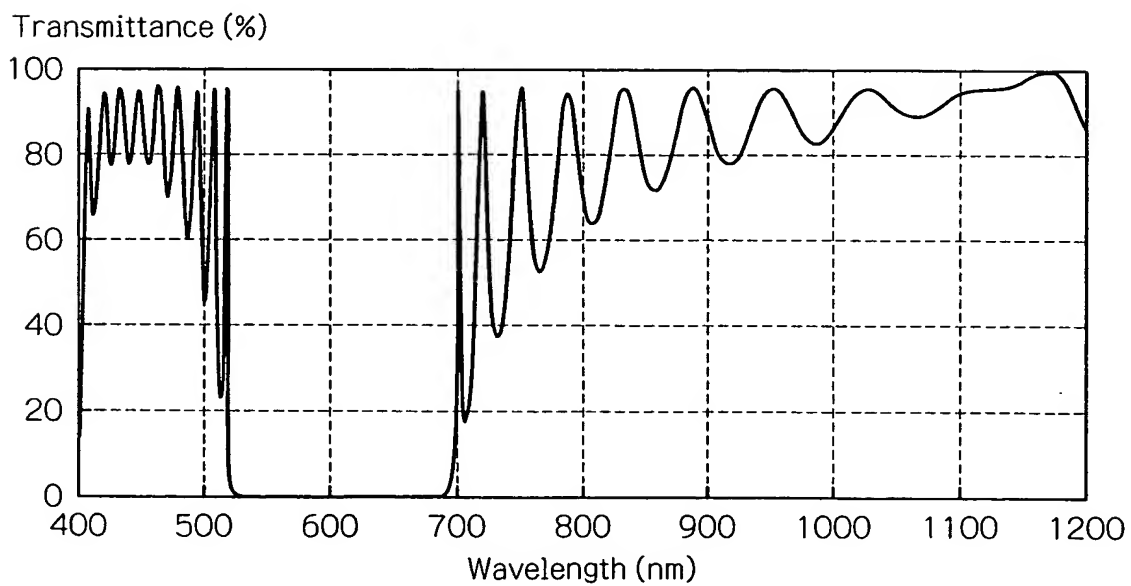


図 1 1 A

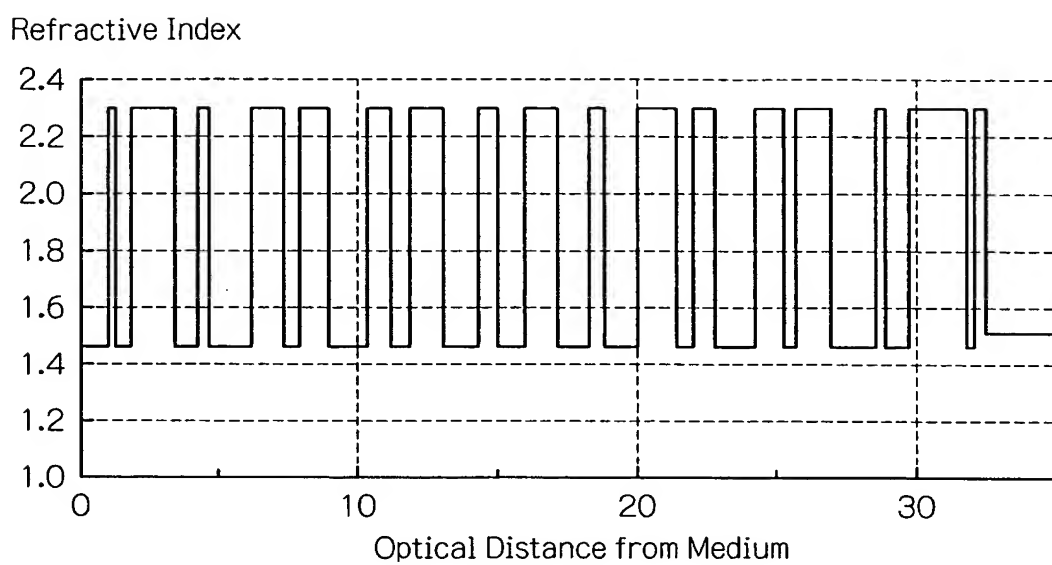


図 1 1 B

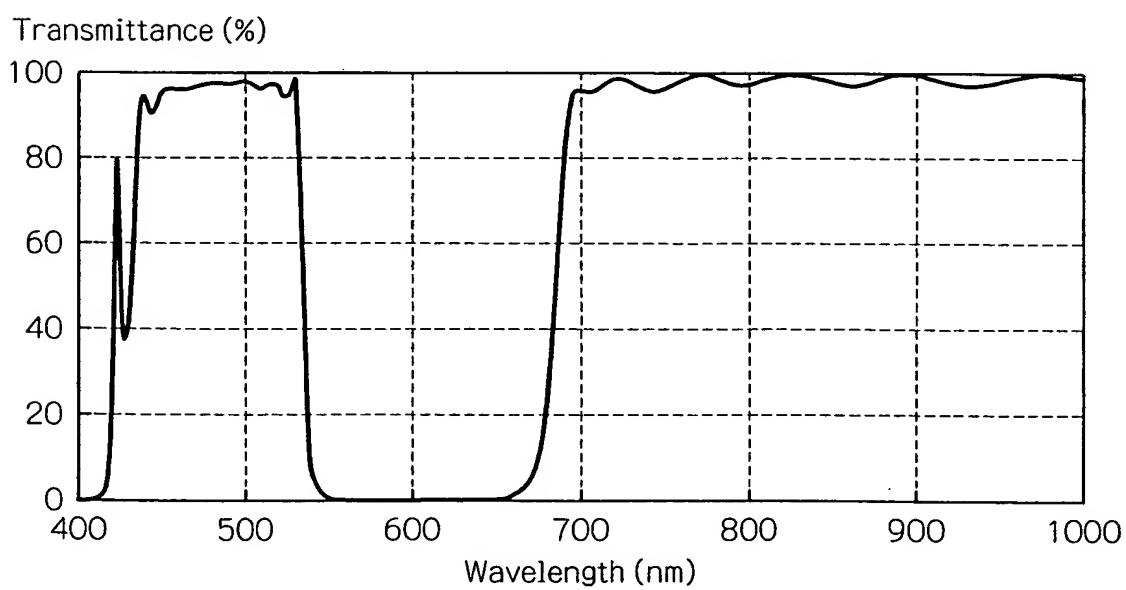


図 1 2 A

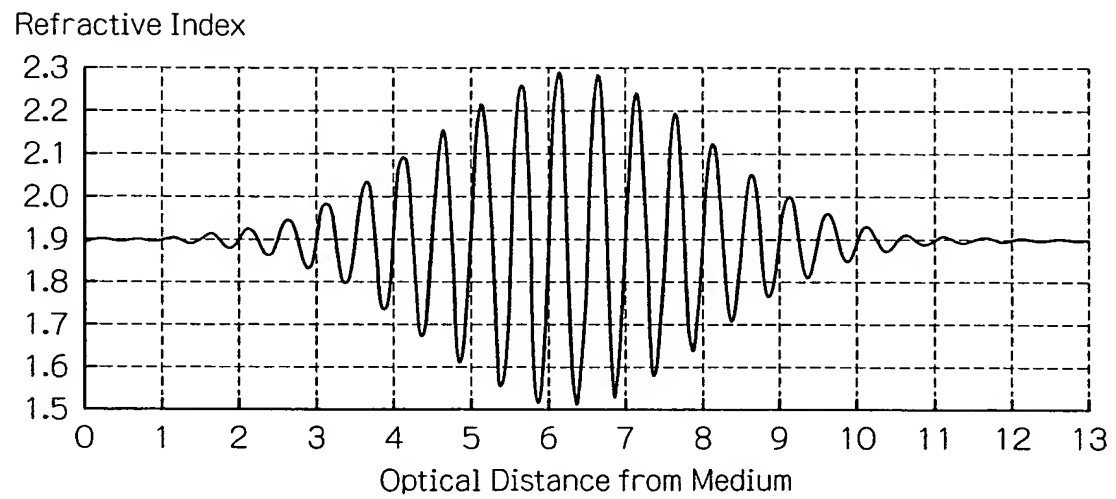


図 1 2 B

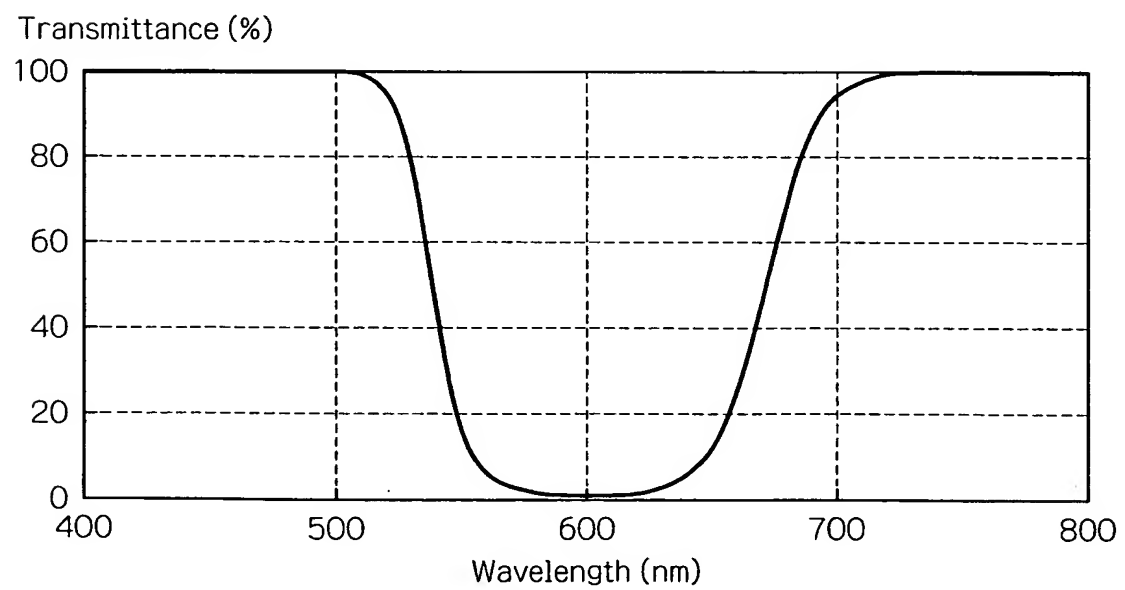


図 1 3 A

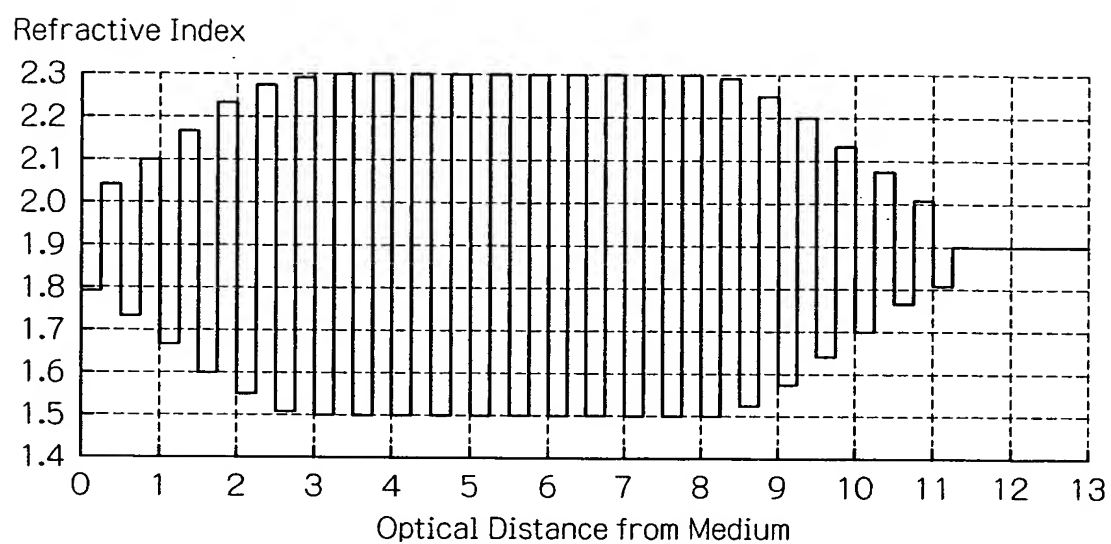


図 1 3 B

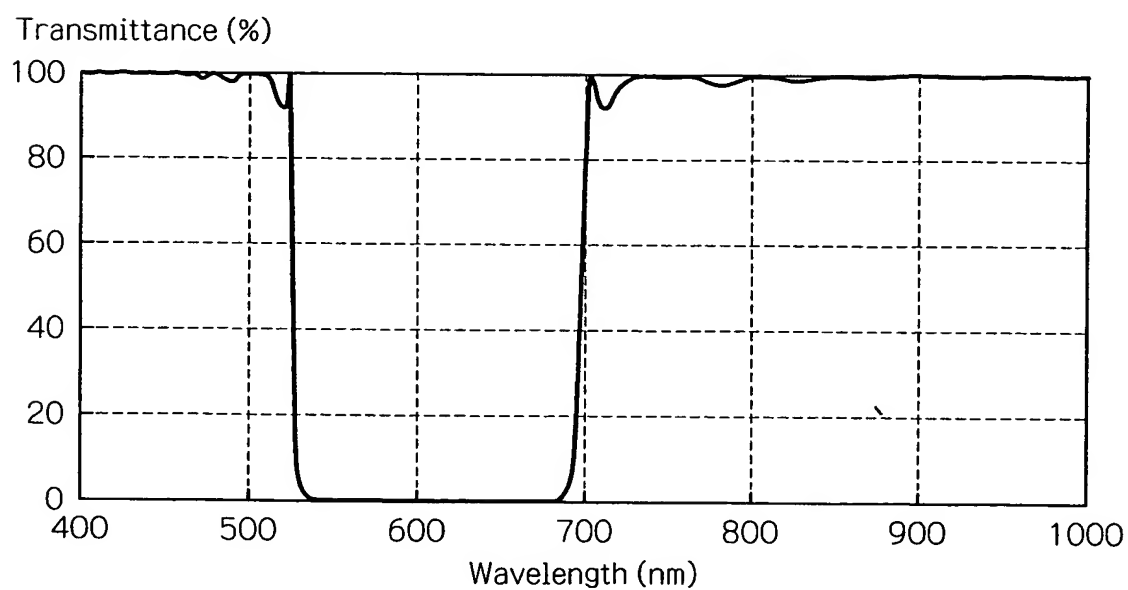


図 1 4 A

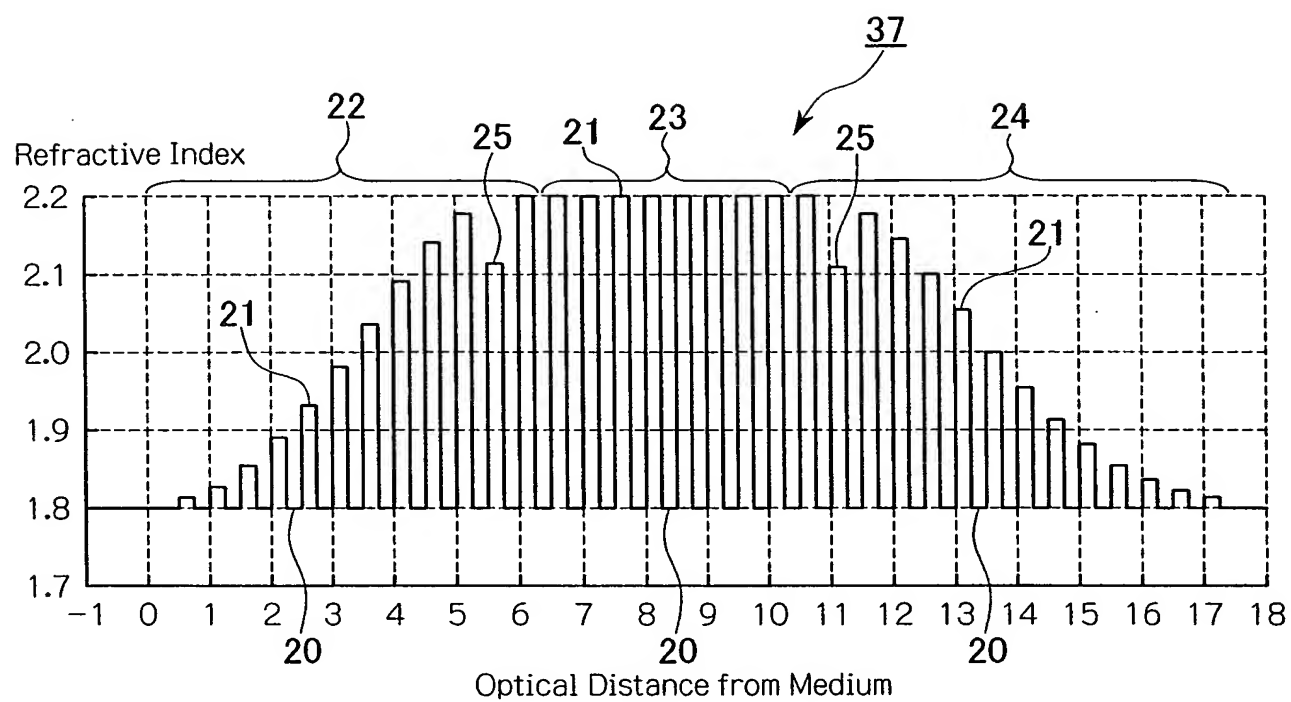


図 1 4 B

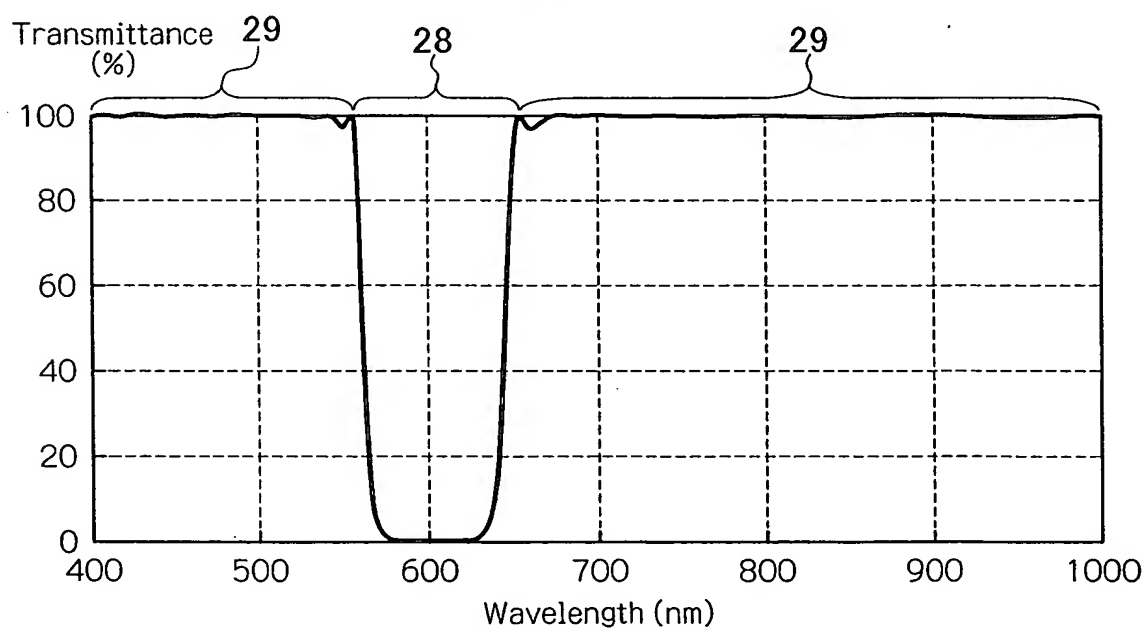


図 1 5 A

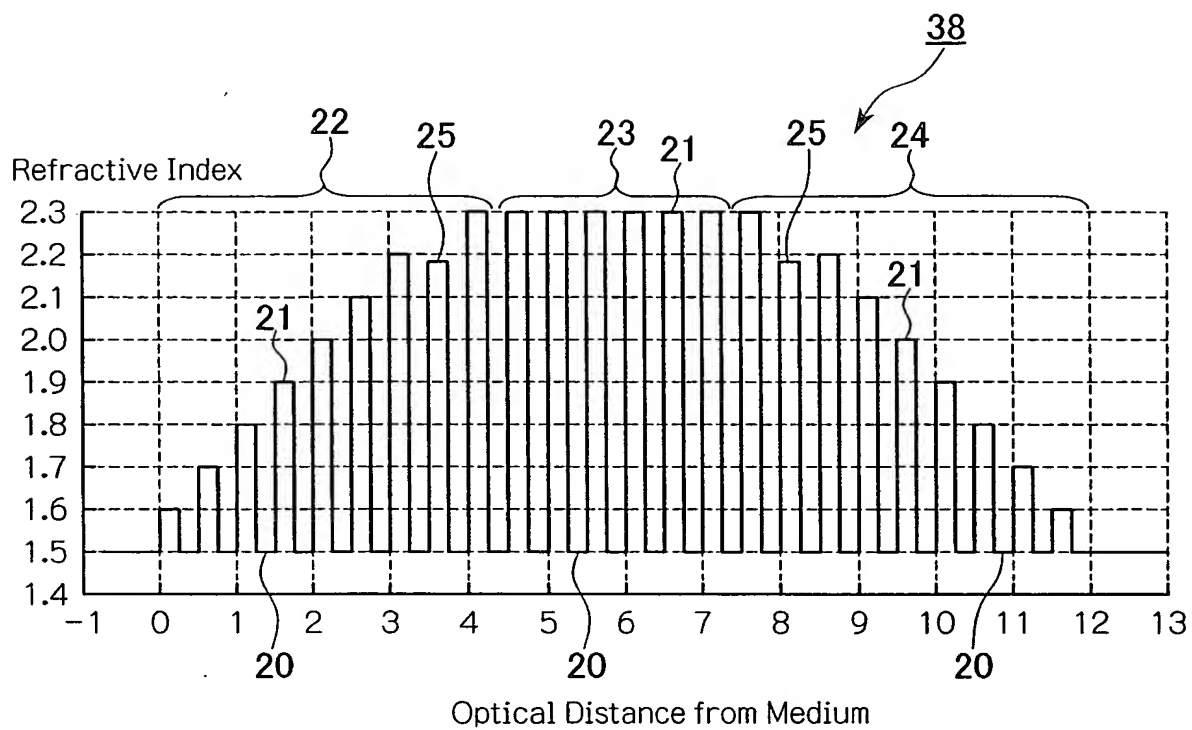


図 1 5 B

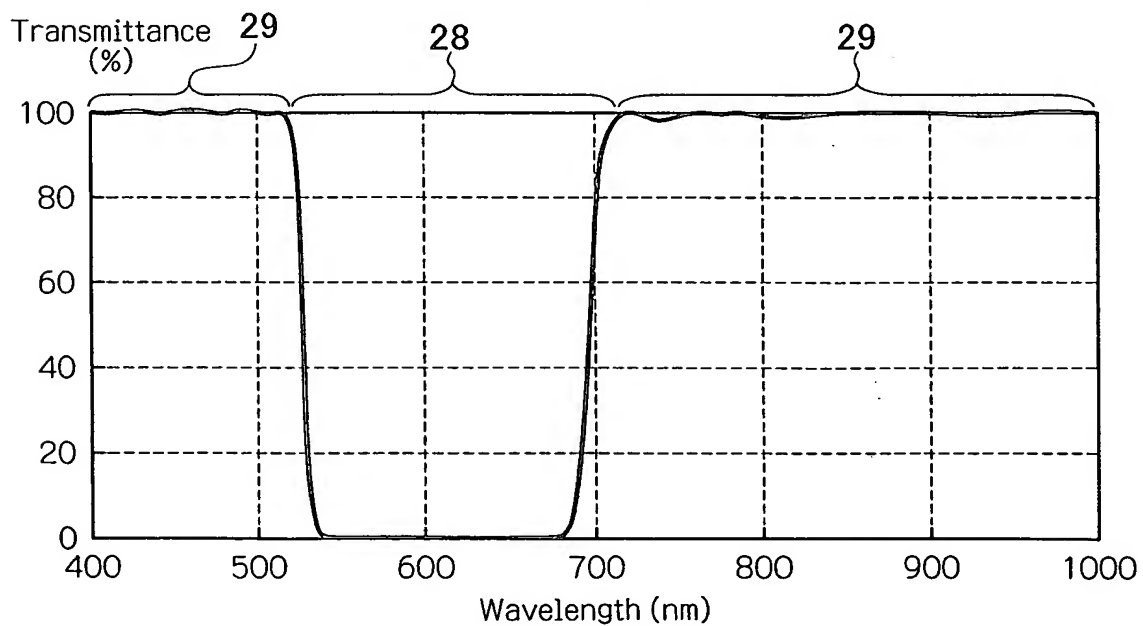


図 1 6 A

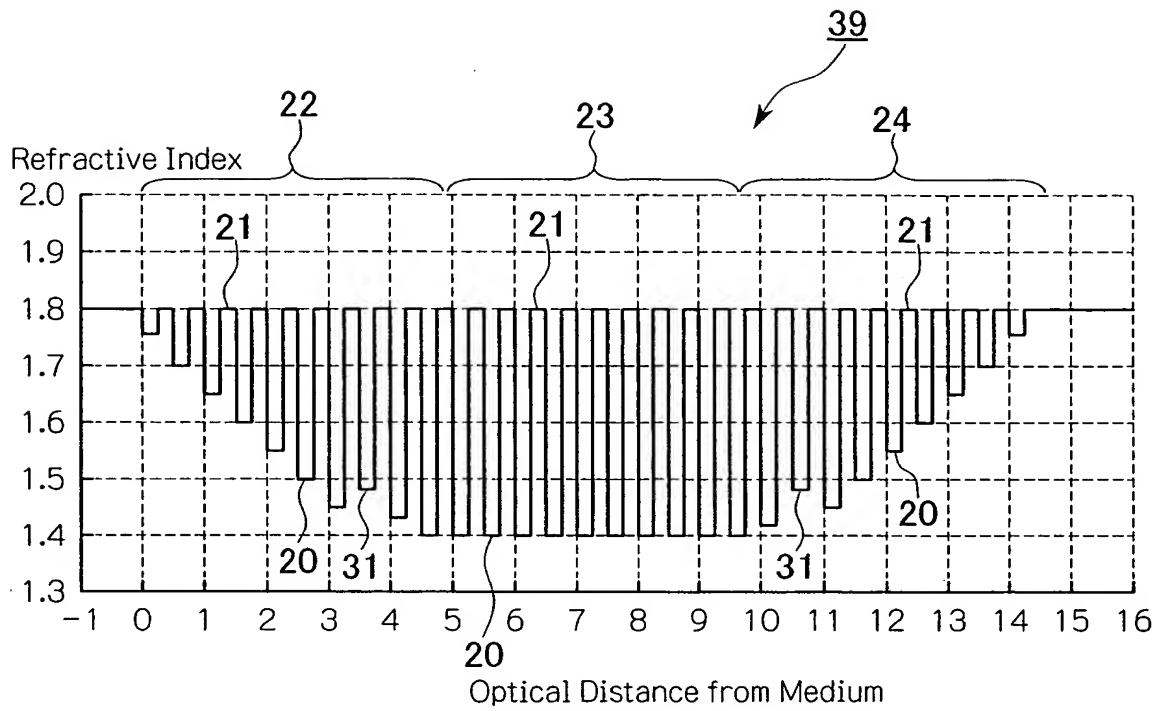


図 1 6 B

